GE-PON-OLT

ST-3502-FX

Overview

The 3502-FX is designed to be placed anywhere which can be powered by either DC or AC source. It has compact 2U box type form factor which enables all front access.

3502-FX comprises of SCU (Switch & Control unit), PSU (Power Supply Unit), and PIU (PON Interface Unit) along with the 2U chassis. The unit has 2 slots for PIU which has 8(eight) 1.25 GE-PON ports on the card. And the SCU of U9016B has 4-port 1000Base-X (SFP) and 2 port 10GBase-R (SFP+) itself for uplink interfaces. The PSUs are hot swappable regardless of powering types.

The PIUs for the 3502-FX are fully compatible with the 3501-FX, which will be a great benefit in reducing CAPEX and OPEX when a service provider builds a PON network with multiple sets of the 3505-FX and the 3502-FX.

Features

* 19 inch Rack mountable chassis structure
* 3 Card Slots
* Hot Swappable Cards
* 1G x 4 ports (SFP), 10G x 2 ports (SFP+)
* 1.25G GE-PON 8 ports per Card
* 1 FAN Interface Module
* Dual Power Supply(AC, DC), Hot Swappable

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Specifications

Hardware

Item	-	Description
Number of PON Interface	**-**	**8 Ports Hot-Swappable card Max 2 Cards**
Network Interfaces	-	4-port 1000Base-X (SFP), 2-port 10GBase-R(SFP+)
Management Interfaces	-	10/100Base-TX, RS-232 (Console)
Power Supply		AC type: 100-240VAC, 50/60Hz DC type : -48VDC
Dimension (WxDxH)	-	Main body: 482mm x 88.8mm x 295mm
Operating Temperature	-	-20~60℃

Software

Features	Description
PON Features	Max 4 bidirectional unicast LLID per ONU Max 256 bidirectional unicast LLID per OLT port Wire speed processing 1.25 Gbps upstream/downstream rate 128-bit Advanced Encryption Standard (AES) encryption engine for PON security and privacy with up to 128 unique keys. AES-128 Downstream Encryption Forward Error Correction(FEC) encoding and decoding Flexible optical transceiver interface for multiple vendor support. Hardware-based configurable Dynamic Bandwidth Allocation (DBA) IEEE 802.1D bridging: 8K MAC Address learning and aging on local interface IEEE 802.1p with four priority queues IEEE 802.1Q VLAN mapping Supports Local and Remote Loop-back testg
L2 Features	802.1Q, Max 4K VLANs, 4K VLAN IDs Private VLAN 802.3ad Link Aggregation Load-balancing based on source and destination MAC/IP 802.1d Spanning Tree Protocol 802.1w Rapid STP Per VLAN STP IGMP v1/v2, Snooping Max 1K Group Support Static Mac Address Port Mirroring
L3 Features	Static Routing RIP, OSPF, BGP Default Gateway VRRP ECMP Max 8 paths PBR (Policy Based Routing) PIM-SM, IGMP v2 Max 1K Group Support DHCP Server/Relay Blocking of illegal IP users DAI (Dynamic ARP Inspection)

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QoS Features	Layer 2: Source/Destination MAC Address, VLAN ID, COS Field Layer 3: Source/Destination IP address, DSCP Layer 4: Source/Destination TCP/UDP port TCP control flag Marking/Remarking: DSCP, COS Packet Drop Mirroring to Port, Redirect to Port Metering, Rate Limiting with 1Mbps unit COS – Queue DSCP - Queue 8 queues per port SPQ, DWRR, Hybrid (SPQ+DWRR) Egress rate shaping per port/queue with 1Mbps unit
Security Features	Netbios, NBT filtering DHCP filtering Packet filtering with ACLs Block the illegal Source MAC address ALL 0's, 1's, System Mac, Default G/W Mac Block the illegal Source IP address Broadcast, DLF, Multicast packet rate control Cut-off of illegal traffic per Source MAC Static Mac address Mac filtering Limitation on Maximum Mac counts Port based Self Loop Detect
System Security Features	RADIUS, TACACS+ Telnet, SNMP with ACL CPU Packet Filtering with ACL Isolate the users who generate overly CPU-intensive Packet TCP sync attack protection with sync cookies CPU packet rate-limit Management packet priority control Gratuitous ARP

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